

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property  
Organization  
International Bureau



(43) International Publication Date  
11 March 2004 (11.03.2004)

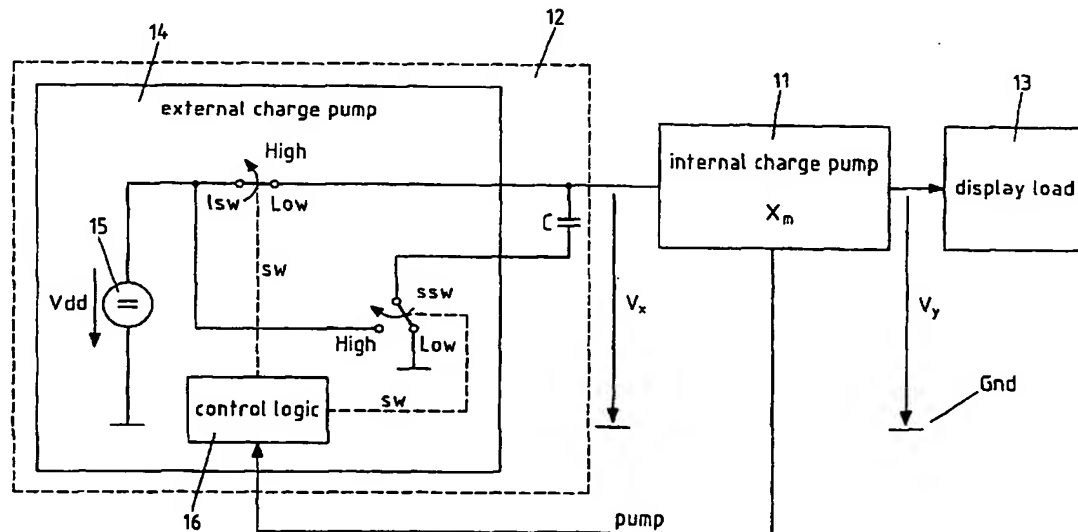
PCT

(10) International Publication Number  
**WO 2004/021553 A1**

- (51) International Patent Classification<sup>7</sup>: **H02M 3/18**, 3/07, 7/25, 3/06
- (21) International Application Number: PCT/IB2003/003430
- (22) International Filing Date: 21 August 2003 (21.08.2003)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:  
02102227.2 28 August 2002 (28.08.2002) EP
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- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).
- Published:  
— with international search report  
— before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

[Continued on next page]

(54) Title: VOLTAGE DOUBLER CIRCUIT



(57) **Abstract:** The invention relates to a voltage processing unit comprising an integrated charge pump 11 for multiplying a voltage  $V_x$  applied to an input of the integrated charge pump 11 by a predetermined factor  $X_m$ . In order to enable a multiplication of an available voltage by a desired factor with a reduced required area for the integrated charge pump, it is proposed that the voltage processing unit further comprises an external voltage doubling circuit 12 for amplifying an available voltage  $V_{dd}$  and for applying the amplified voltage  $V_x$  to the input of the integrated charge pump 11. The invention relates equally to a corresponding method.